

HITACHI

FOR MESSRS : _____

DATE : Aug.25,2003

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP10Q010-T CONTENTS

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- When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.
- This product is inhibited to apply in any life support instrument.

ACCEPTED BY: _____

PROPOSED BY: Jimmy Ho

	Sh. No.	7B64PS 2701- SP10Q010-T-5	PAGE	1-1/1
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RECORD OF REVISION

DATE	SHEET No.	SUMMARY
Jan.30,'03	7B64PS 2703-SP10Q010-T-2 PAGE 3-1/1	3. GENERAL SPECIFICATION Deleted (12) Touch
	7B64PS 2709-SP10Q010-T-2 PAGE 9-1/2	9. OUTLINE DIMENSIONS Note 4. Unmark Tolerance : $\pm 0.2\text{mm}$ ↓ Revised Note 4. Unmarked Tolerance : $\pm 0.5\text{mm}$
Apr.02,'03	7B64PS 2707-SP10Q010-T-3 PAGE 7-1/1	7. BLOCK DIAGRAM Revised BLOCK DIAGRAM.
	7B64PS 2709-SP10Q010-T-3 PAGE 9-1/2	9. OUTLINE DIMENSIONS Revised pixel of scan.
May.09,'03	7B64PS 2707-SP10Q010-T-4 PAGE 7-1/1	7. BLOCK DIAGRAM Revised scanning pixel of Y side
	7B64PS 2709-SP10Q010-T-4 PAGE 9-1/2	9. OUTLINE DIMENSIONS Revised pixel of scan.
Aug.25,'03	7B63PS 2709-SP10Q010-T-5 PAGE 9-1/2	9. OUTLINE DIMENSIONS 1. Changed the FPC exiting the module from the "center" to the rear side of module. 2. Changed dimension between FPC (upper side) to module from (5.0) to (2.0).

3. GENERAL SPECIFICATIONS

(1) Part Name	SP10Q010-T
(2) Module Size	94.7 (W)mm x 73.3 (H)mm x 5.5 (D)mm
(3) Active Area	76.785(W)mm x 57.585(H)mm
(4) Dot Pitch	0.24 (W)mm x 0.24 (H)mm
(5) Dot Size	0.225 (W)mm x 0.225 (H)mm
(6) Resolution	320 (W) x 240 (H) dots
(7) Duty Ratio	1/242
(8) Bias Ratio	1/13
(9) LCD Type	Transflective type B/W F-STN (Positive Mode)
(10) Viewing Direction	6 O'clock
(11) Backlight	LED(Color : White)

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

VSS=0V:STANDARD

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	7.0	V	
Power Supply for LC Drive	VLCD	0	30.0	V	
Input Voltage	Vi	-0.3	VDD+0.3	V	(Note 1,2)

Note 1 : $\overline{\text{DOFF}}$, FLM , CL1, CL2 , D0~D3 , M.

Note 2 : Make certain you are grounded when handling LCM.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

I T E M	OPERATING		STORAGE		COMMENT
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-20°C	70°C	-30°C	80°C	(Note 2,3,4)
Humidity	(Note 1)		(Note 1)		Without condensation
Vibration	-	2.45 m/s ²	-	11.76 m/s ² (Note 5)	1 h max . (Note 6)
Shock	-	29.4 m/s ²	-	490 m/s ² (Note 5)	XYZ directions 11ms (Note 6)
Corrosive Gas	Not acceptable		Not acceptable		

Note 1 : $T_a \leq 40^\circ\text{C}$: 85%RH max.

$T_a > 40^\circ\text{C}$: Absolute humidity must be lower than the humidity of 85%RH at 40°C

Note 2 : T_a at -30°C < 48h , at 80°C < 168h.

Note 3 : Background color changes slightly depending on ambient temperature .
This phenomenon is reversible.

Note 4 : This LCM will be operated under low temperature , and the response time will be slower.

Note 5 : This module should be operated normally after finish the test.

Note 6 : The module do not have mounting hole.

It should be fixed by the way of sandwiching-like method.

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

I T E M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for Logic	VDD-VSS	-	2.5	3.3	4.5	V
Power Supply Voltage for LCD Driving	VLCD-VSS	-	-	-	30.0	V
Input Voltage (Note 1)	VI	H level	0.8VDD	-	VDD	V
		L level	0	-	0.2VDD	V
Power Supply Current For Logic , (Note 2)	IDD	VDD-VSS=3.3V VLCD-VSS=(22.5)V	-	(2.38)	-	mA
Power Supply Current for LC Driving , (Note 2)	ILCD (Note 4)	VDD-VSS=3.3V VLCD-VSS=(22.5)V	-	(1.5)	-	mA
Recommended LC Driving Voltage (Note 3)	VLCD-VSS	Ta= 0°C , $\phi=0^\circ$	-	(24.5)	-	V
		Ta=25°C , $\phi=0^\circ$	-	(22.5)	-	V
		Ta=50°C , $\phi=0^\circ$	-	(20.5)	-	V
Frame Frequency (Note 4)	fFLM	-	70	75	80	Hz

Note 1 : $\overline{\text{DOFF}}$, FLM , CL1 , CL2, D0~D3.

Note 2 : fFLM=75Hz , Test pattern is all "Q".

VLCD-VSS=(22.5)V, Ta=25°C.

Note 3 : Recommended LC driving voltage fluctuate about $\pm 1.0\text{V}$ by each module.
Test pattern is all "Q".

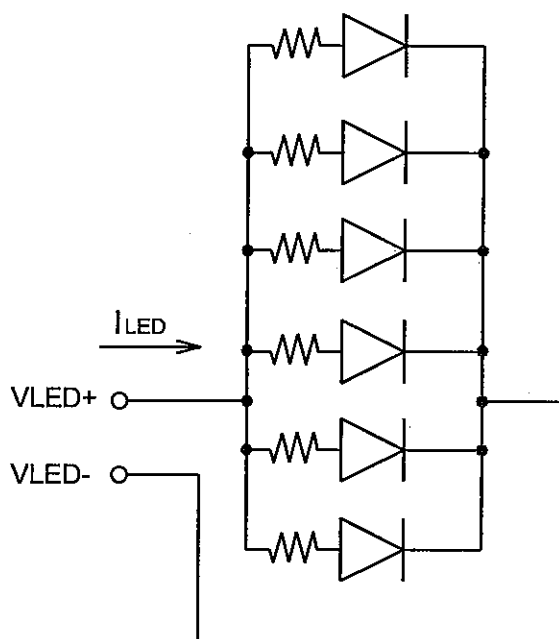
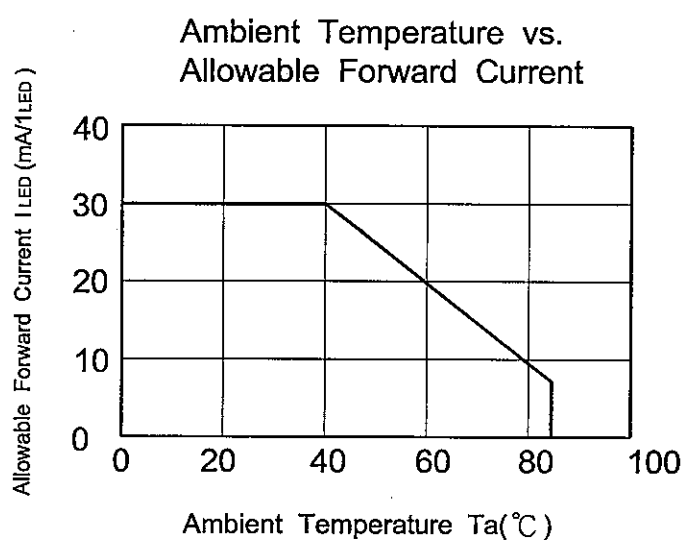
Note 4 : Need to make sure of flicking and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT

$T_a=25^{\circ}\text{C}$ (Display off)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for LED	VLED	-	-	5.0	5.2	V
Power Supply Current for LED	ILED	VLED=5.0V	-	110	120 (Note 1)	mA

Note 1 : The ILED changes depending on ambient temperature.



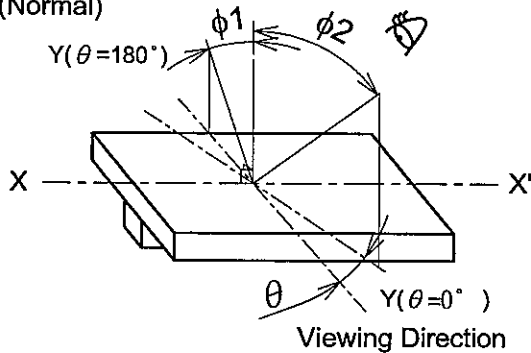
6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25 °C (Backlight on)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing Angle	$\phi_2 - \phi_1$	$K \geq 2.0$	-	80	-	deg.	1,2
Contrast Ratio	K	$\phi = 0^\circ, \theta = 0^\circ$	-	(5)	-	-	3
Response Time (Rise)	tr	$\phi = 0^\circ, \theta = 0^\circ$	-	(150)	-	ms	4
Response Time (Fall)	tf	$\phi = 0^\circ, \theta = 0^\circ$	-	(350)	-	ms	4

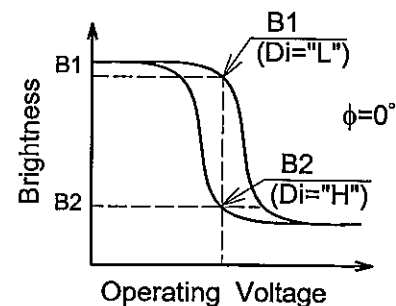
Note 1 : Definition of θ and ϕ
(Normal)



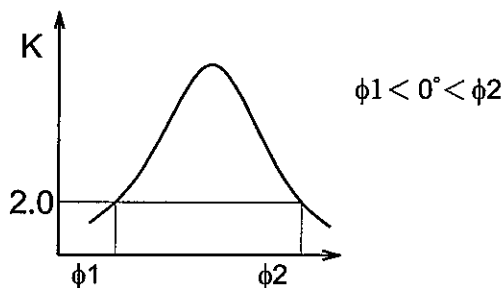
(Measure condition by HITACHI)

Note 3 : Definition of contrast "K"

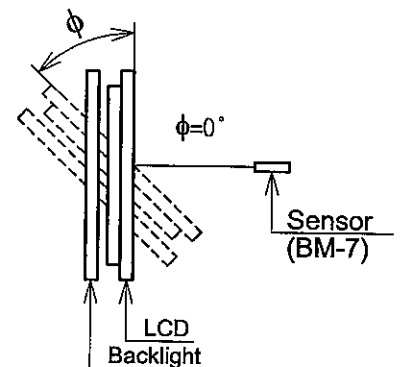
$$K = \frac{\text{Brightness on non-selected dot (B1)}}{\text{Brightness on selected dot (B2)}}$$



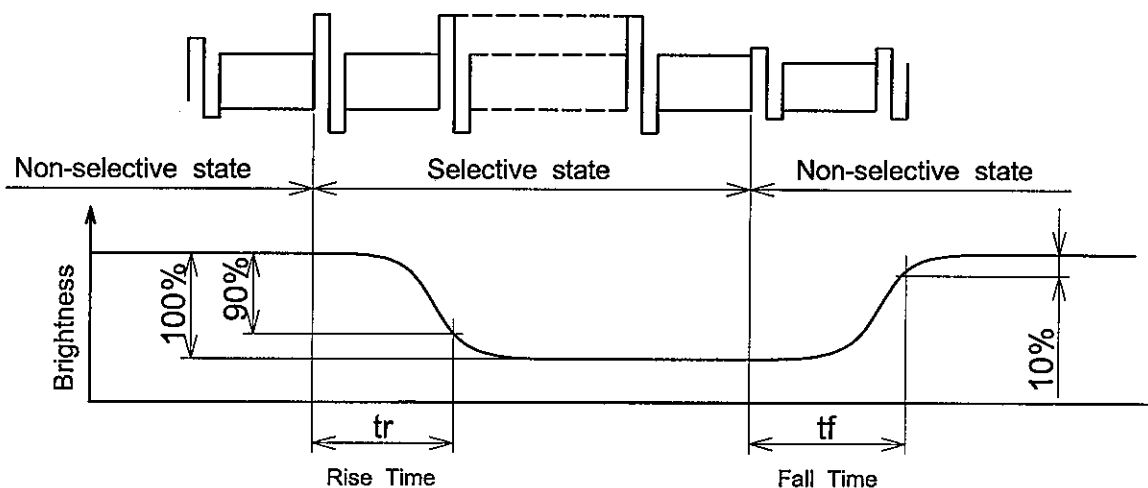
Note 2 : Definition of viewing angle ϕ_1 and ϕ_2



Contrast ratio K vs viewing angle ϕ



Note 4 : Definition of optical response

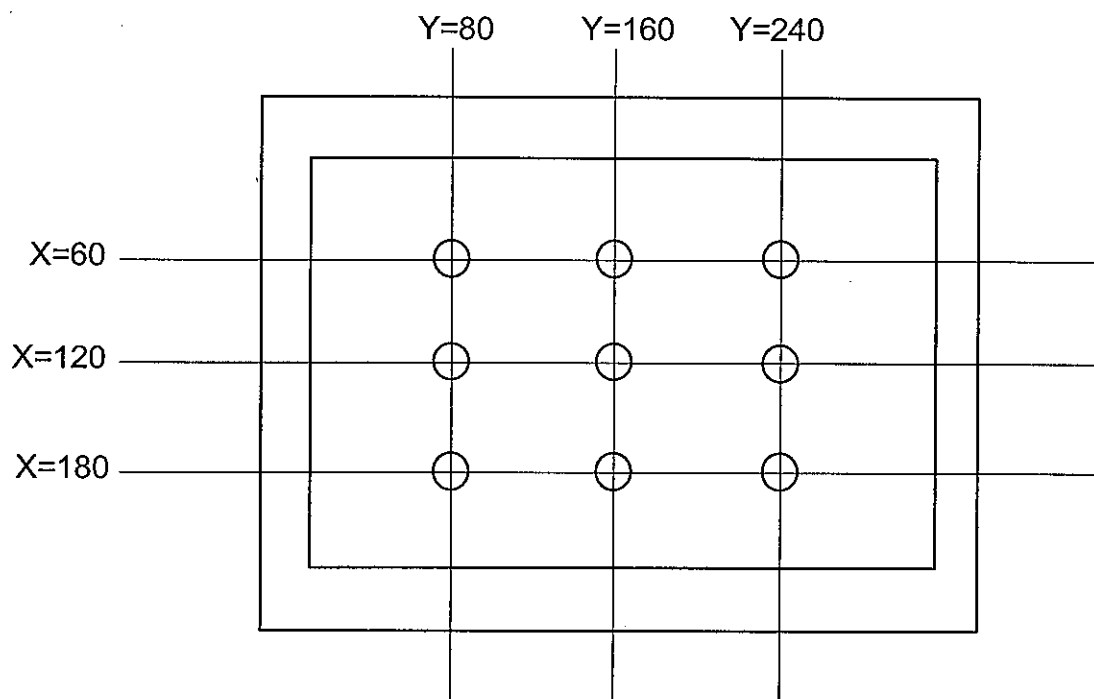


6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

(LCM, BACKLIGHT ON, Ta=25°C)

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness Uniformity	-	-	±35	%	(Note 1,2)
Brightness	-	60	-	cd/m ²	I _{LED} =110 mA

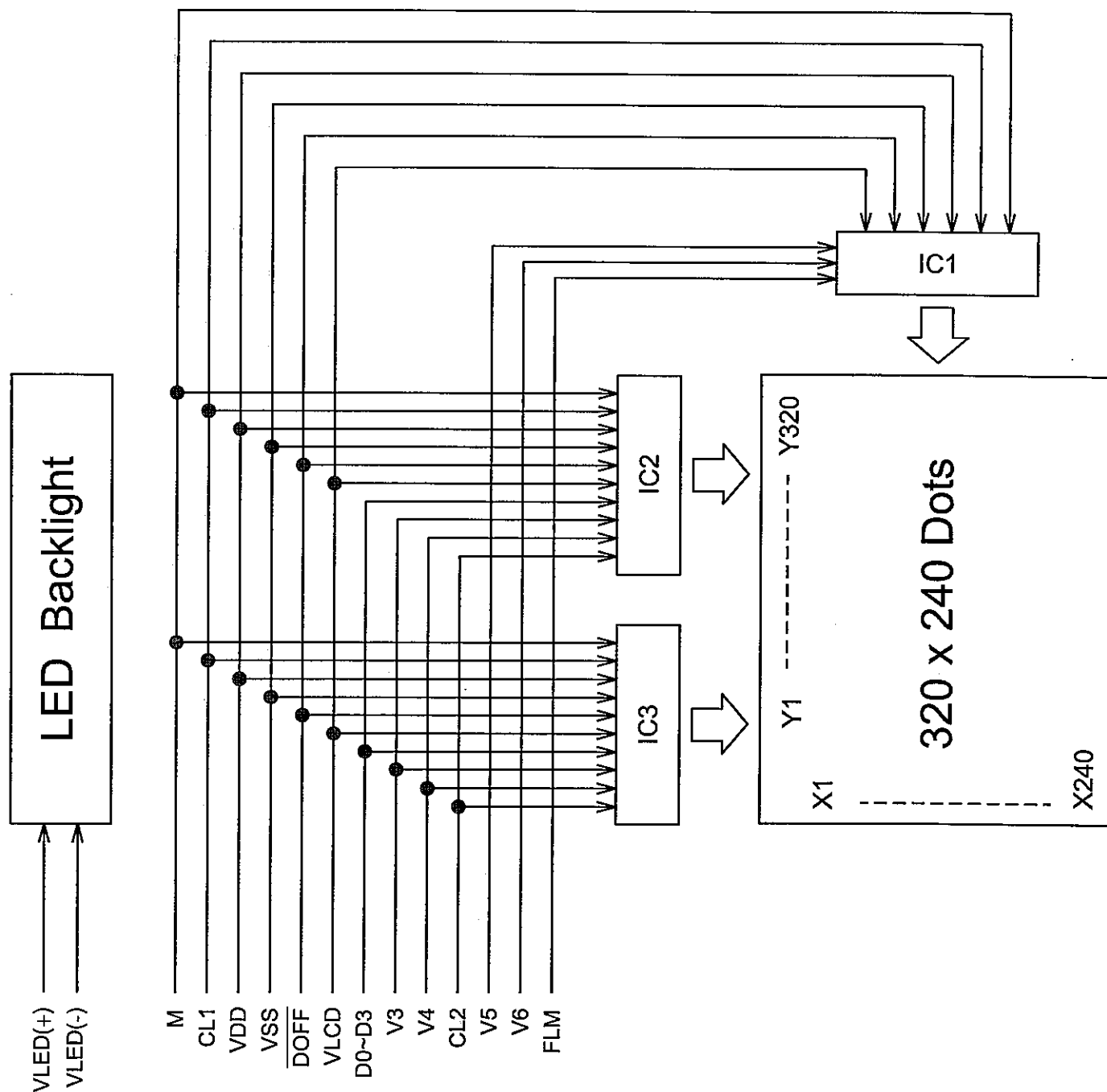
Note 1 : Measure of the following 9 places on the display.



Note 2 : Definition of brightness tolerance.

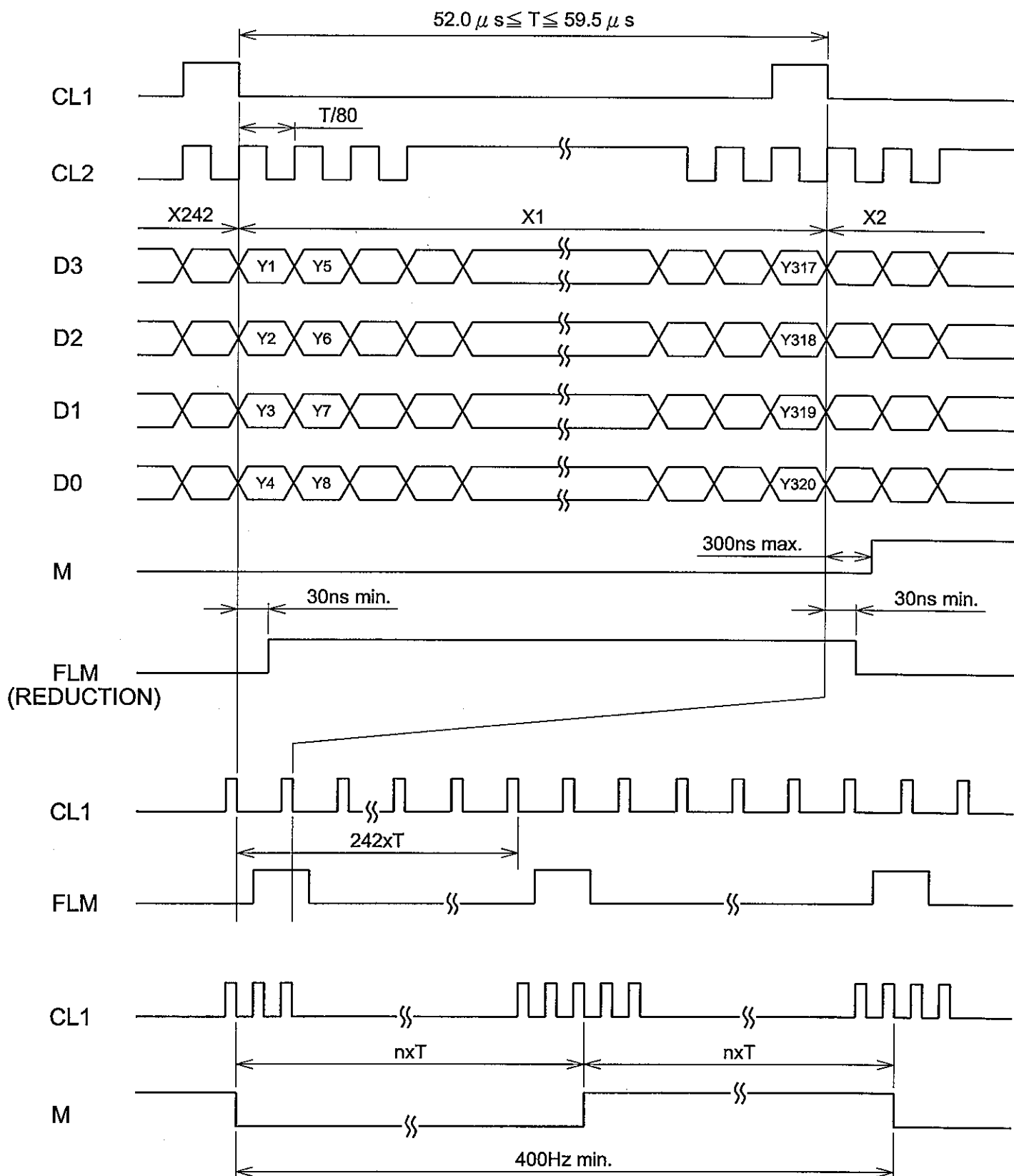
$$\left(\frac{\text{Max. or Min. Brightness} - \text{Average Brightness}}{\text{Average Brightness}} \right) \times 100\%$$

7. BLOCK DIAGRAM



8. INTERFACE TIMING

8.1 TIMING CHART (4-BITS PARALLEL DATA INPUT)



Note 1 : M signal should be kept 400Hz min. and 50 % duty.

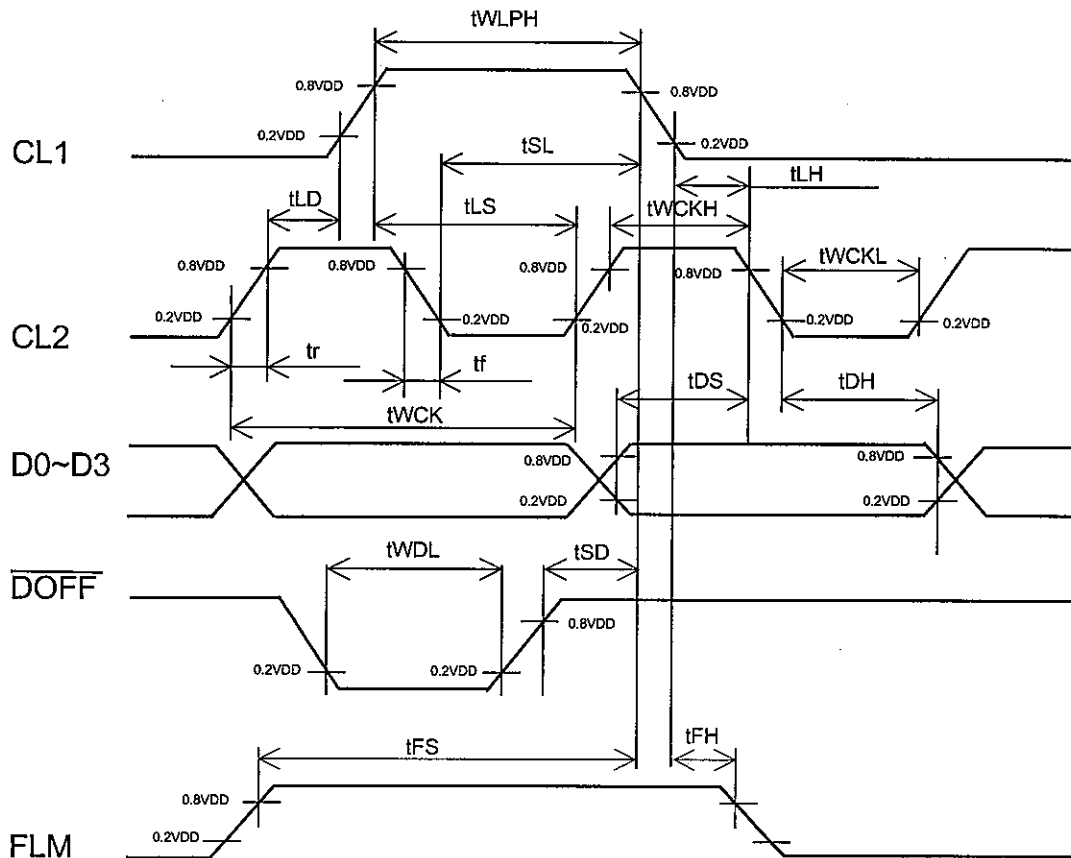
Note 2 : HITACHI recommend $n \times \text{CL1}$ pulses of M signal 50 %.

$n=13$

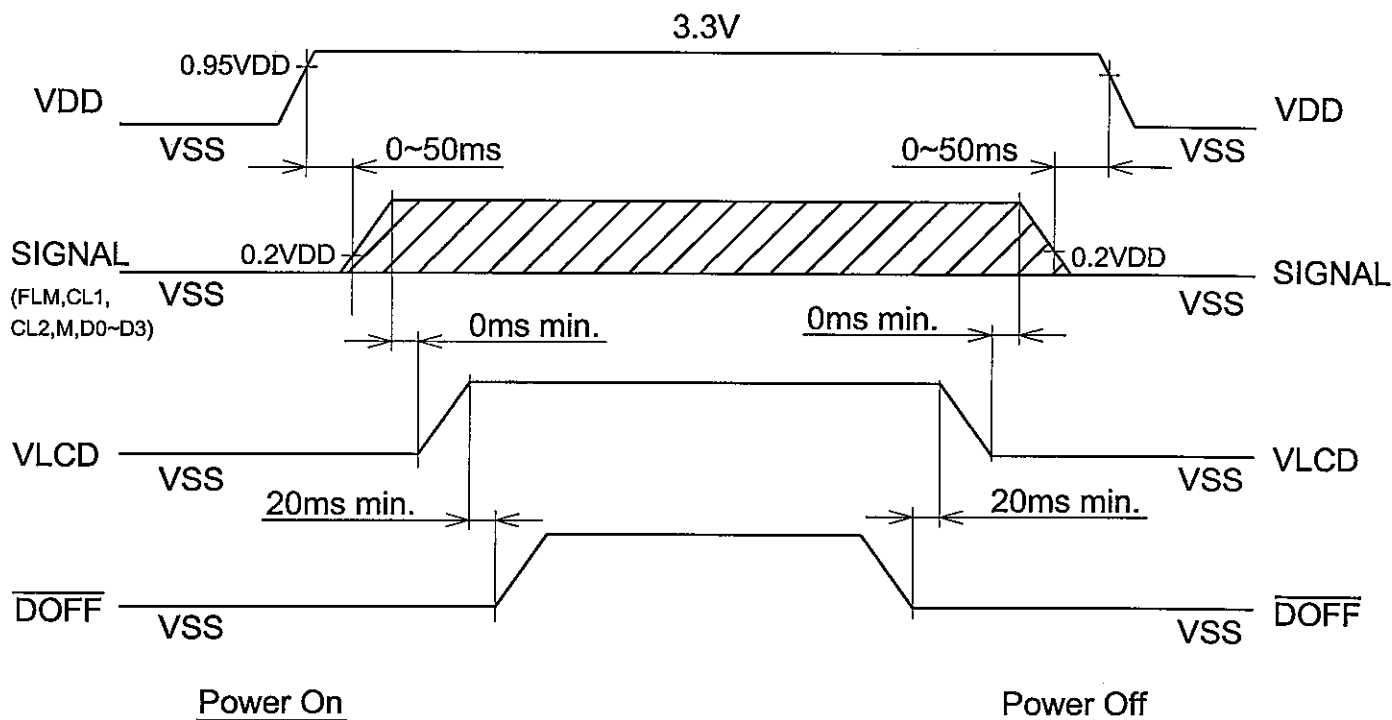
8.2 TIMING CHARACTERISTICS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
Shift Clock Period	tWCK	125	-	-	ns	$t_r, t_f \leq 11\text{ns}$
Shift Clock "H" Pulss Width	tWCKH	51	-	-	ns	
Shift Clock "L" Pulss Width	tWCKL	51	-	-	ns	
Data Setup Time	tDS	30	-	-	ns	
Data Hole Time	tDH	40	-	-	ns	
Latch Pulse "H" Pulse Width	tWLPH	51	-	-	ns	
Shift Clock Rise to Latch Pulse Rise Time	tLD	0	-	-	ns	
Shift Clock Rise to Latch Pulse Fall Time	tSL	51	-	-	ns	
Latch Pulse Rise to Shift Clock Rise Time	tLS	51	-	-	ns	
Latch Pulse Fall to Shift Clock Fall Time	tLH	51	-	-	ns	
Input Signal Rise Time	t _r	-	-	50	ns	(Note 1)
Input Signal Fall Time	t _f	-	-	50	ns	(Note 1)
DOFF Removal Time	tSD	100	-	-	ns	
DOFF Enable Pulse Time	tWDL	1.2	-	-	μs	
"FLM" Set Up Time	tFS	100	-	-	ns	-
"FLM" Hold Time	tFH	30	-	-	ns	-

Note 1 : $(t_{WCK} - t_{WCKH} - t_{WCKL}) / 2$ is the maximum in the case of high speed operation.



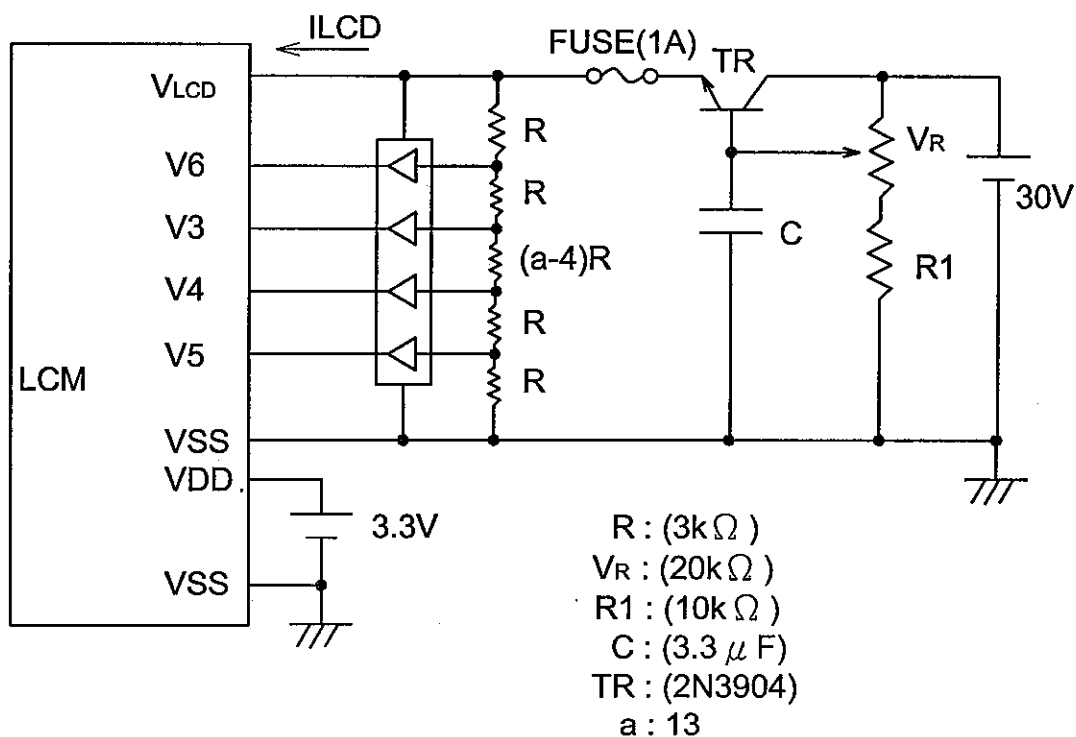
8.3 TIMING OF POWER SUPPLY AND INTERFACE SIGNAL



Note 1 : $\overline{\text{DOFF}}$ function takes priority even if the input signal status becomes irregular immediately after VDD power-on.

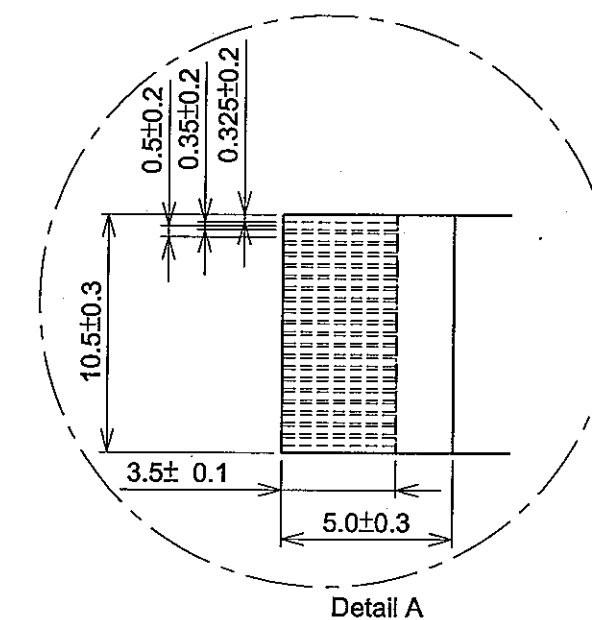
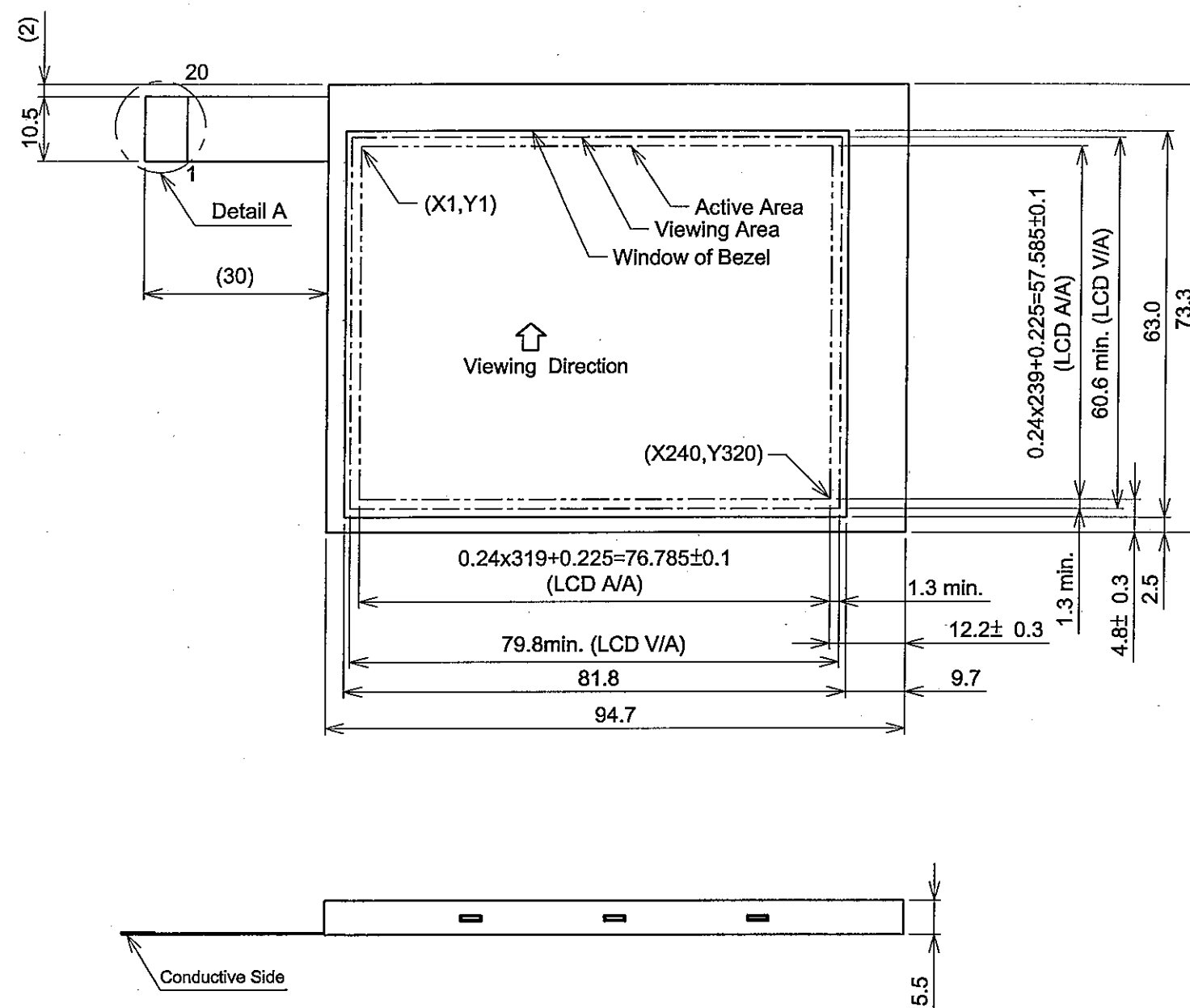
Note 2 : Please keep the specified sequence because wrong sequence may cause permanent damage to the LCM.

8.4 POWER SUPPLY FOR LCM



9. OUTLINE DIMENSIONS

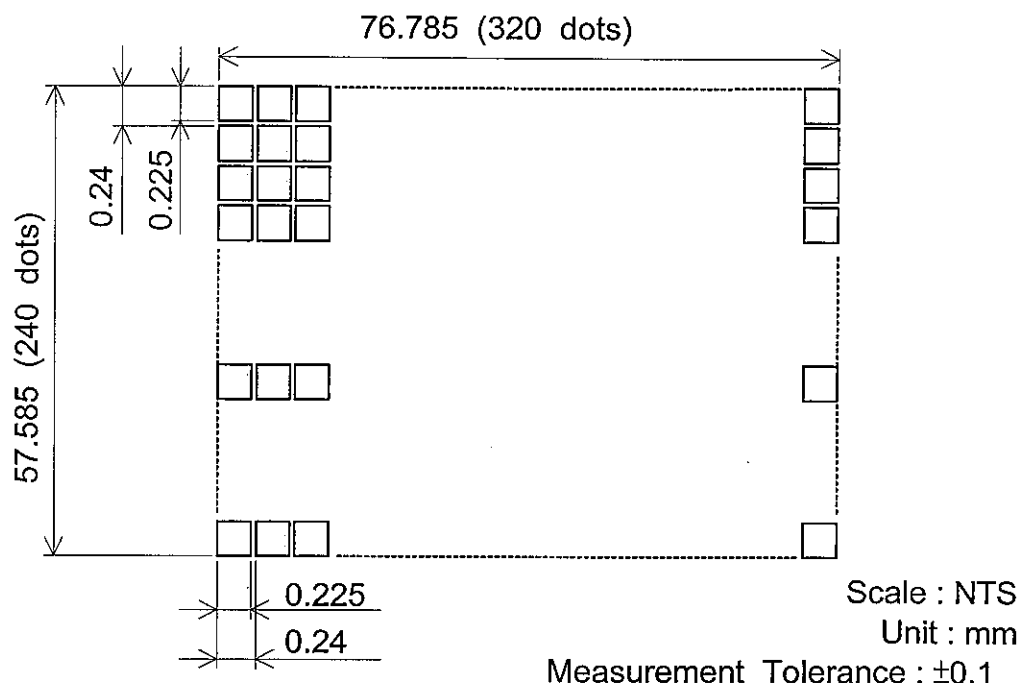
9.1 OUTLINE DIMENSIONS



Notes:

1. Reference mark : ()
2. Unit : mm
3. Scale : NTS
4. Unmarked Tolerance : ± 0.5mm
5. Measurement when adding 9.8x10⁴ Pa at the measuring point

9.2 DISPLAY PATTERN



9.3 INTERFACE PIN CONNECTION

9.3.1 CN1 : LCM I/F (0.5mm PITCH , 20PINS FPC)

PIN No.	SYMBOL	FUNCTION
1	FLM	Frame start signal data signal of the shift register of the Com driver
2	V6	Bias voltage for non-select (Com driver)
3	VDD	Power supply for logic
4	V5	Bias voltage for non-select (Com driver)
5	DOFF	Hi : Display on ; Low : Display off
6	VSS	Ground
7	M	Switch signal to convert LCD driver waveform into AC
8	CL1	1) Latch pulse of display data 2) Shift clock for Com driver
9	VLCD	Power supply for LCD
10	V3	Bias voltage for non-select (Seg driver)
11	VDD	Power supply for logic
12	V4	Bias voltage for non-select (Seg driver)
13	VSS	Ground
14	CL2	Clock pulse for Seg shift
15	D0	Input data signal
16	D1	Input data signal
17	D2	Input data signal
18	D3	Input data signal
19	VLED(+)	Power supply for LED backlight
20	VLED(-)	Power supply for LED backlight

Suitable connector : Molex / 52745-2090 or 52746-2090

10. QUALITY STANDARD

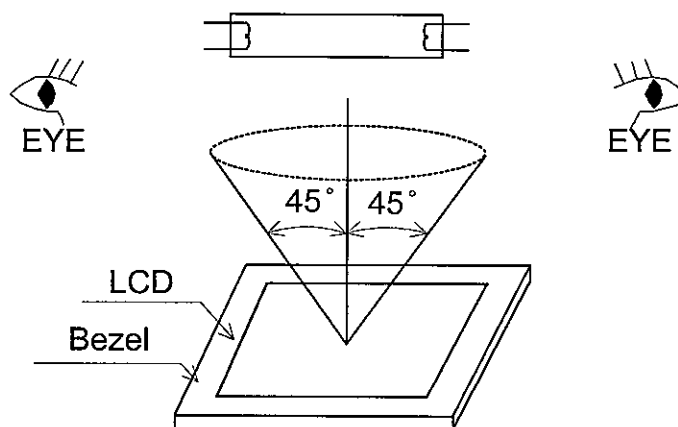
10.1 APPEARANCE INSPECTION CONDITIONS (IN THE VIEWING AREA)

Visual inspection under single 20W fluorescent lamp with eyes to LCD distance 25cm and lamp to LCD distance 25 to 30cm.

Viewing angle should be smaller than 45° .

The line of sight for inspection shall be inside the half section of a cone which consisted of line segment 45° to the axis with vertex at the center of LCD.

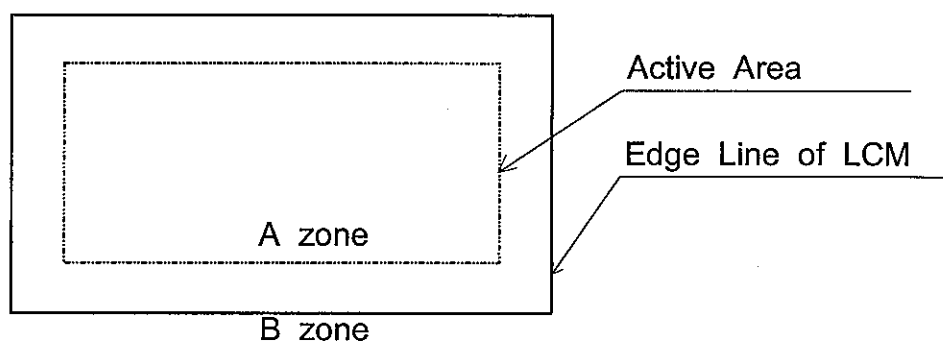
The cone axis perpendicular to the LCD and passing through the fluorescent lamp.



10.2 DEFINITION OF EACH ZONE

A zone : Within the active area specified at page 9-1/2 of this document.

B zone : Area between the edge line of LCM and the active area line specified at page 9-1/2 of the document.



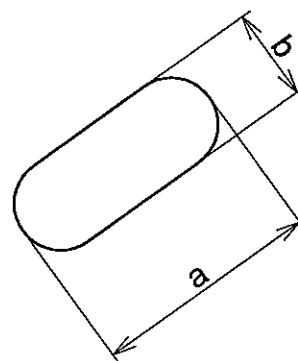
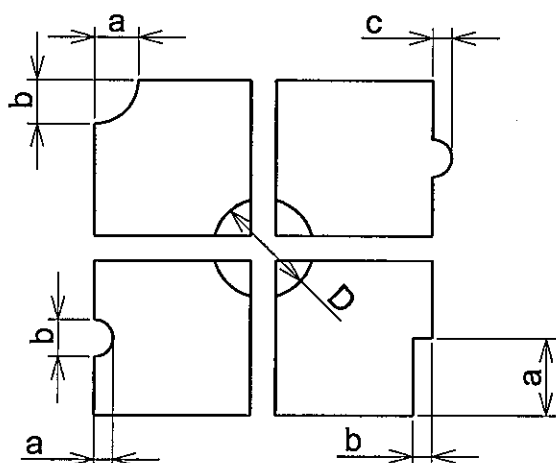
10.3 APPEARANCE SPECIFICATION

*) If the problem occurs about this item, the responsible person of both party (customer and HITACHI) will discuss more detail.

No.	ITEM	CRITERIA				A	B	
L C D	Scratches	Distinguished one is not acceptable (to be judge by HITACHI limit sample)				*	-	
	Dent	Same as above				*	-	
	Wrinkles in Polarize	Same as above				*	-	
	Bubbles	Average diameter D(mm)		Maximum number acceptable		○	-	
		D≤0.2		Ignore				
		0.2<D≤0.3		12				
		0.3<D≤0.5		3				
		0.5<D		None				
	(Note 1)							
	Stains, Foreign Materials Dark Spot	Filamentous				○	*	
		Length L(mm)	Width W(mm)	Maximum number acceptable				
		L≤2.0	W≤0.03	Ignore				
		L≤3.0	0.03<W≤0.05	6				
		-	0.05<W	None				
		Round				○	*	
		Average diameter D(mm)	Maximum number acceptable	Minimum space				
		D≤0.2	Ignore	-				
		0.2<D≤0.33	8	10 mm				
		0.33<D	None	-				
		The whole number	Filamentous + Round = 10					
		(Note 1,2)	Those wiped out easily are acceptable				○	-
		Color Tone	To be judged by HITACHI limit sample				○	-
	Color Uniformity	Same as above				○	-	
	Pinhole	Average diameter D(mm)		Maximum number acceptable		○	-	
		D≤0.15		Ignore				
		0.15<D≤0.3		10				
		0.3<D		None				
(Note 1)								
Contrast Irregularity (Spot)	Average diameter D(mm)	Contrast	Maximum number acceptable	Minimum space				
	D≤0.25	To be Judge by HITACHI	Ignore	-				
	0.25<D≤0.35		10	20mm				
	0.35<D≤0.5		4	20mm				
	(Note 1)		0.5 < D	None			-	

No.	ITEM	CRITERIA				A	B
L C D	Contrast Irregularity (Lone) (A Pair of Scrathc)	Width W(mm)	Length L(mm)	Maximum acceptable umber	Minimum space	○	-
		$W \leq 0.25$	$L \leq 1.2$	2	20mm		
		$W \leq 0.2$	$L \leq 1.5$	3	20mm		
		$W \leq 0.15$	$L \leq 2.0$	3	20mm		
		$W \leq 0.1$	$L \leq 3.0$	4	20mm		
	(Note 2)	The whole number ≤ 6					
	Rubbing Scratch	To be judged by HITACHI limit standard				○	-

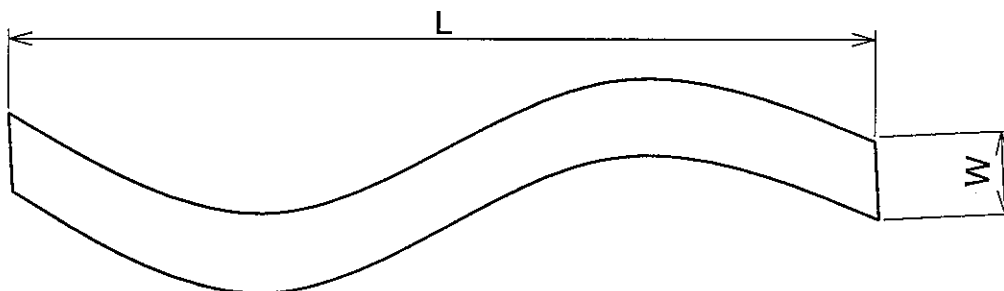
Note (1)



$$\frac{a+b}{2} = D \dots \text{Average Diameter}$$

c...Salient

(2) Definition of length L and width W



11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VLCD) AND VIEWING ANGLE RANGE.

Setting VLCD out of the recommended condition will be a cause for a change of viewing angle range.

11.2 CAUTION AGAINST STATIC CHARGE

As this module is provided with C-MOS LSI, the care to take such a precaution as to grounding the operator's body is required when handling it.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage ($3.3 \pm 10\%$). If above sequence is not kept, C-MOS LSI of LCD modules may be damaged due to latch up problem.

11.4 PACKING

- (1) No leaving products is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35° or higher, special care to prevent them from high humidity is required.
A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer pell-off. Please keep the temperature and humidity within the specified range for use and storage.
- (2) Since upper polarizers and lower aluminum to be easily damaged, they should be handled with full care so as not to get them touched, pushed or rubbed by a piece of glass, tweezers and anything else which are harder than a pencil lead 3H.
- (3) As the adhesives used for adhering upper/lower polyester and aluminum plates are made of organic substances which will deteriorated by a chemical reaction with such chemicals as acetone, ethanol and isopropyl alcohol. The following solvents are recommended for use:
normal hexane
Please contact us when it is necessary for you to use chemicals other than the above.

- (4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface and contact terminals due to coldness will be cause for polarizer damage, stain and dirt on product.
When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.
- (7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands.
(There are some cosmetics detrimental to polarizers.)
- (8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery. Because be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPERATION

- (1) It is an indispensable condition to drive LCD within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's show dark blue Color in then however those phenomena do not mean Malfunction or out of order with LCD's which will come back in the specified operation temperature range.
- (3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit.
Please operate the LCD module under the relative condition of 40°C 85%RH.

11.6 STORAGE

In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways are recommended.

- (1) Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it, and with no desiccant.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is, keeping temperature in the range from 0°C to 35°C
- (3) Storing with no touch on polarizer surface by anything else.
(It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

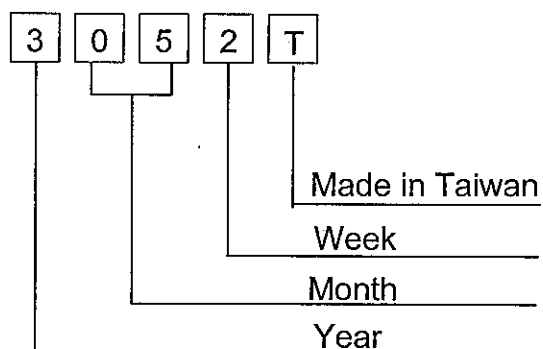
11.7 SAFETY

- (1) It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damage glass cell comes in contact with your hands, please wash it off well with soap and water.

12. DESIGNATION OF LOT MARK

Lot Mark

Lot mark is consisted of 4 digital number



YEAR	FIGURE IN LOT MARK
2002	2
2003	3
2004	4
2005	5
2006	6

Note 1 : Some products have alphabet at the end or the first.

MONTH	FIGURE IN LOT MARK	MONTH	FIGURE IN LOT MARK
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

WEEK (DAY IN CALENDAR)	FIGURE IN LOT MARK
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

Location of lot mark : On the back side of LCM

3052T

13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any requests , please contact HITACHI.