

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(•) Final Specification

Title 15.0" XGA TFT LCD

BUYER	MAGIC LCD
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC150X01
MODEL	SL02

*When you obtain standard approval, please use the above model name without suffix

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Please return 1 copy for your c your signature and cor		TV Product Engineering Dept. LG. Philips LCD Co., Ltd		

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RECORD OF REVISIONS

Revision No	Revision Date	Page	DESCRIPTION
1.0	May,03,2005	-	Final
		4,17	Changed luminance spec.
		9	Added interlace image sticking option
		16	Changed T2 min spec.
		17	Changed contrast ratio spec.

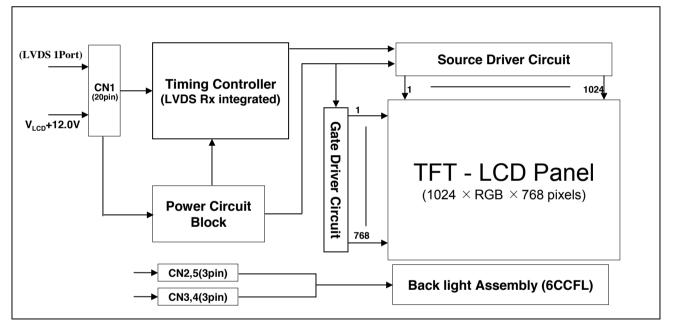


1. General Description

LC150X01 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 15.0 inch diagonally measured active display area with XGA resolution (768 vertical by 1024 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M(True) colors.

It has been designed to apply the 8Bit 1 port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, and high color depth are important.



General Features

Active Screen Size	15.0 inches(38.016cm) diagonal (Aspect ratio 4:3)
Outline Dimension	332.8(H) x 262.8 (V) x 22.0(D) mm (Typ.)
Pixel Pitch	0.297mm x 0.297mm
Pixel Format	1024 horiz. by 768 vert. Pixels RGB strip arrangement
Color Depth	8bit, 16,7 M colors
Luminance, White	410 cd/m ² (Center 1 points Typ.)
Viewing Angle (CR>10)	Viewing Angle Free (R/L 178(Typ.), U/D 178(Typ))
Power Consumption	Total 25.8 Watt(Typ.) (2.3@VLCD,23.5@ 410 cd/m ² / LAMP=7mA])
Weight	2100 g (Тур.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H) & Anti-glare (Haze 13%) treatment of the front polarizer

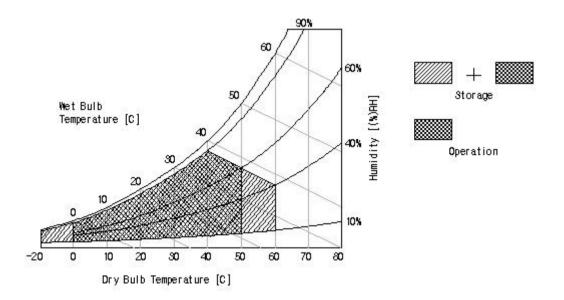
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Valu	les	Units	Notes
i arameter	Symbol	Min	Max	Onits	Notes
Power Input Voltage	VLCD	-0.3	+14.0	Vdc	at 25 \pm 3 $^{\circ}$ C
Operating Temperature	Тор	0	+50	°C	1
Storage Temperature	Тѕт	-20	+60	°C	1
Operating Ambient Humidity	Нор	10	90	%RH	1
Storage Humidity	Нsт	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.



3. Electrical Specifications

3-1. Electrical Characteristics

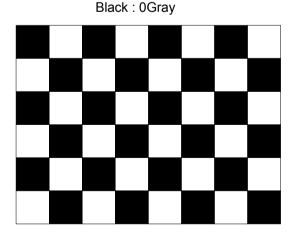
It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCDs.

Table 2_1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
	Gymbol	Min	Тур	Max	5 mil	Notes
MODULE :						
Power Supply Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Dowor Supply Input Current	ILCD	-	192	250	mA	1
Power Supply Input Current		-	238	309	mA	2
Power Consumption	PLCD	-	2.3	3.0	Watt	1
Rush current	Irush	-	-	3.0	А	3

Note :

- 1. The specified current and power consumption are under the V_{LCD}=12.0V, 25° C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).



White: 255Gray

Mosaic Pattern(8 x 6)



Table 2_2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Values			Unit	Notes
		Oymbol	Min	Тур	Max	Onic	NOICS
LAMP :		-					
Operating Voltage	1	VBL	550(7.5mA)	560(7mA)	655(3mA)	V _{RMS}	1, 2
Operating Current		IBL	3.0	7.0	7.5	mA _{RMS}	1
Established Startir	Established Starting Voltage						1, 3
	at 25 °C				870	V_{RMS}	
	at 0°C				1,130	V_{RMS}	
Operating Frequ	ency	fBL	45	60	80	kHz	4
Discharge Stabilization Time		Ts			3	Min	1, 5
Power Consumption		PBL		23.52	25.86	Watt	6
Life Time			50,000			Hrs	1, 7

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

- The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.
- Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.
- 1. Specified values are for a single lamp.
- 2. Operating voltage is measured at $25 \pm 2^{\circ}$ C. The variance of the voltage is $\pm 10\%$.
- The voltage above V_S should be applied to the lamps for more than 1 second for start-up. (Inverter open voltage must be more than lamp starting voltage.)
 Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 4. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 5. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.

 T_{S} is the time required for the brightness of the center of the lamp to be not less than 95%.

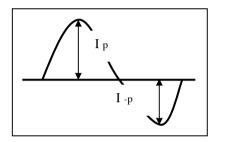
- 6. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current. ($P_{BL} = V_{BL} \times I_{BL} \times N_{Lamp}$)
- 7. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^{\circ}$ C.

8. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.

Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.

It shall help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2}$ $\pm10\%.$
 - * Inverter output waveform had better be more similar to ideal sine wave.



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* Asymmetry rate:
| I_p – I_{-p} | / I_{rms} x 100%
* Distortion rate
I_p (or I_{-p}) / I_{rms}

- 9. The inverter which is combined with this LCM, is highly recommended to connect coupling(ballast) condenser at the high voltage output side. When you use the inverter which has not coupling(ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.
- 10.In case of edgy type back light with over 4 parallel lamps, input current and voltage wave form should be synchronized



3-2. Interface Connections

- LCD Connector(CN1):DF14H-20P-1.25H (Manufactured by Hirose) or Equivalent

- Mating Connector : DF14H-20S-1.25C (Manufactured by Hirose) or Equivalent

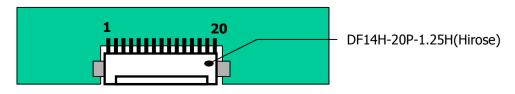
Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No	Symbol	Description	Output Pin # (LVDS Tx)
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	GND	Power Ground.	
4	GND	Power Ground	
5	RXIN0-	LVDS Receiver Signal(-)	PIN#48
6	RXIN0+	LVDS Receiver Signal(+)	PIN#47
7	GND	Ground	
8	RXIN1-	LVDS Receiver Signal(-)	PIN#46
9	RXIN1+	LVDS Receiver Signal(+)	PIN#45
10	GND	Ground	
11	RXIN2-	LVDS Receiver Signal(-)	PIN#42
12	RXIN2+	LVDS Receiver Signal(+)	PIN#41
13	GND	Ground	
14	RXCLK IN-	LVDS Receiver Clock Signal(-)	PIN#40
15	RXCLK IN+	LVDS Receiver Clock Signal(+)	PIN#39
16	GND	Ground	
17	RXIN3-	LVDS Receiver Signal(-)	PIN#38
18	RXIN3+	LVDS Receiver Signal(+)	PIN#37
19	Mode	Interlace image sticking option(+3.3V/GND)	Note 5
20	GND	Ground	Note 1

Note: 1. 20nd Pin should be ground.

- 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 3. All VLCD (power input) pins should be connected together.
- 4. Input Level of LVDS signal is based on the IEA 664 Standard.
- 5. GND : 1 Frame Inversion, +3.3V : 2 Frame Inversion

Rear view of LCM



May. 03, 2005



Table 4. REQUIRED SIGNAL ASSIGNMENT FOR LVDS Transmitter (DS90C385 or Compatible)

Pin #	Pin Name	Symbol	Description	Output
51	TxIN0	Red0 [LSB]	Red Pixel Data	
52	TxIN1	Red1	Red Pixel Data	
54	TxIN2	Red2	Red Pixel Data	Rx0- Rx0+
55	TxIN3	Red3	Red Pixel Data	
56	TxIN4	Red4	Red Pixel Data	
2	TxIN5	Red7 [MSB]	Red Pixel Data	Rx3- Rx3+
3	TxIN6	Red5	Red Pixel Data	Rx0-
4	TxIN7	Green0 [LSB]	Green Pixel Data	Rx0+
6	TxIN8	Green1	Green Pixel Data	Rx1-
7	TxIN9	Green2	Green Pixel Data	Rx1+
8	TxIN10	Green6	Green Pixel Data	Rx3-
10	TxIN11	Green7 [MSB]	Green Pixel Data	Rx3+
11	TxIN12	Green3	Green Pixel Data	
12	TxIN13	Green4	Green Pixel Data	Rx1-
14	TxIN14	Green5	Green Pixel Data	Rx1+
15	TxIN15	Blue0 [LSB]	Blue Pixel Data	
16	TxIN16	Blue6	Blue Pixel Data	Rx3-
18	TxIN17	Blue7 [MSB]	Blue Pixel Data	Rx3+
19	TxIN18	Blue1	Blue Pixel Data	Rx1- Rx1+
20	TxIN19	Blue2	Blue Pixel Data	
22	TxIN20	Blue3	Blue Pixel Data	Rx2-
23	TxIN21	Blue4	Blue Pixel Data	Rx2+
24	TxIN22	Blue5	Blue Pixel Data	
25	TxIN23	RES		Rx3- Rx3+
27	TxIN24	Hsync.	Horizontal Sync signal	
28	TxIN25	Vsync.	Vertical Sync signal	Rx2- Rx2+
30	TxIN26	EN	Data Enable	
50	TxIN27	Red6	Red Pixel Data	Rx3- Rx3+

Notes: 1. Refer to LVDS Transmitter Data Sheet for detail descriptions.

2. 7 means MSB and 0 means LSB at R,G,B pixel data.

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Table 5. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (DS90C385 or Compatible)

Host System	DS90C385			Timing				
24 Bit	or Compatible	DF14H-2	DF14H-20P-1.25H					
RED0	51							
RED1	52 TxOUT0-	48	5 400.0 \$	RxIN0-				
RED2	54 TxOUT0+	47 6	100 ♀ ≶	RxIN0+				
RED3	55							
RED4	56							
RED5		46 8	3 400.0 \$	RxIN1-				
RED6	50 TxOUT1+	45	100 ♀ ≶	— RxIN1+				
RED7	2							
GREEN0	4							
GREEN1	6 TxOUT2-	42 1	1 400.0 \$	RxIN2-				
GREEN2	7 TxOUT2+	41 1	2 100 ♀ ≶	RxIN2+				
GREEN3	11							
GREEN4	12							
GREEN5	14 TxCLKOUT-	40 1	4	RxCLKIN-				
GREEN6	8 TxCLKOUT+	39 1	5 100 Ω ≶	RxCLKIN+				
GREEN7	10							
BLUE0	15							
BLUE1	19 TxOUT3-	38 1	7					
BLUE2	20 TxOUT3+	37 1	⁷ 100 Ω ≶	RxIN3+				
BLUE3	22							
BLUE4	23							
BLUE5	24							
BLUE6	16							
BLUE7	18							
Hsync	27							
Vsync	28							
Data Enable								
CLOCK	31		LCD M	odule				

Note : 1. The LCD Module uses a 1000hm resistor between positive and negative lines of each receiver input.



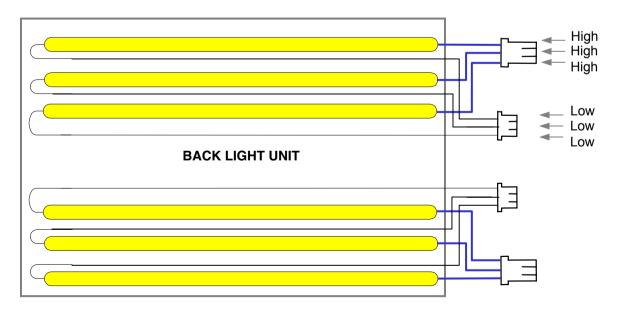
Table 6. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3,CN4,CN5)

The backlight interface connector is a model PHR-3(CN3/CN4) and BHR-03VS-1(CN2/CN5) manufactured by JST. The mating connector part number are S 3B-PH-KL (3pin), SM03(4.0)B-BHS-1-TB (3pin) or equivalent. The pin configuration for the connector is shown in the table below.

No	Pin	Symbol	Description	Notes
	1	H1	Power supply for lamp 1(High voltage side), White	1
CN2	2	H2	Power supply for lamp 2 (High voltage side), Sky Blue	1
	3	H3	Power supply for lamp 3 (High voltage side), Gray	1
	1	L1	Power supply for lamp 1 (Low voltage side), White	2
CN3	2	L2	Power supply for lamp 2 (Low voltage side), Blue	2
	3	L3	Power supply for lamp 3 (Low voltage side), Black	2
	1	L4	Power supply for lamp 4 (Low voltage side), White	2
CN4	2	L5	Power supply for lamp 5 (Low voltage side), Blue	2
	3	L6	Power supply for lamp 6 (Low voltage side), Black	2
	1	H4	Power supply for lamp 4 (High voltage side), White	1
CN5	2	H5	Power supply for lamp 5 (High voltage side), Sky Blue	1
	3	H6	Power supply for lamp 6 (High voltage side), Gray	1

Notes: 1. The high voltage power terminal is colored white, sky blue, and gray.

2. The low voltage pin color is white, dark blue, and black .





3-3. Signal Timing Specifications

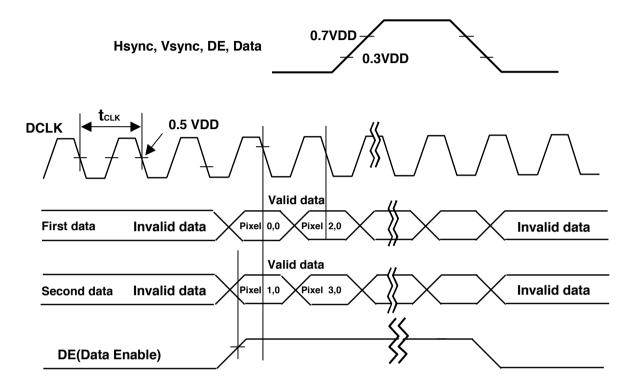
This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

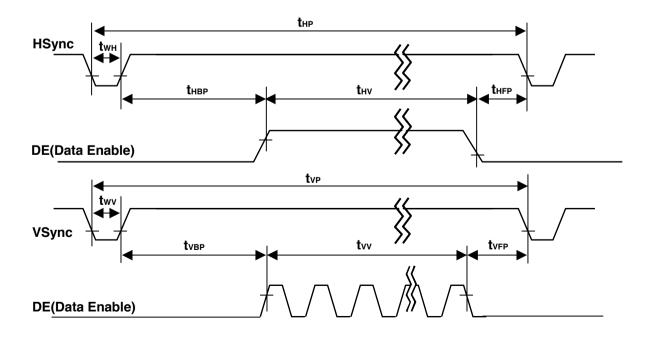
ITEM	Symbol		Min	Тур	Мах	Unit	Note	
DOLK	Period		12.2	15.4	20.0	ns		
DCLK	Frequency	-	50	65	82	MHz		
	Period	tHP	1104	1344	1592	tclk		
Hsync	Frequency	fн	-	48.36	-	KHz		
	Width	twн	16	136	-	tськ		
	Period	t∨P	774	806	-	tHP	Note 1) PAL : 47~53Hz NTSC : 57~63Hz	
Vsync	Frequency	f∨	47	60	63	Hz		
	Width	tw∨	2	6	-	tHP		
	Horizontal Valid	tн∨	1024	1024	1024			
	Horizontal Back Porch	tнвр	16	160		tour		
	Horizontal Front Porch	thep	8	24		tclk		
DE	Horizontal Blank	-	80	320	thp- thv			
(Data Enable)	Vertical Valid	t∨∨	768	768	768			
	Vertical Back Porch	tvвр	2	29				
	Vertical Front Porch	tVFP	2	3		tHP		
	Vertical Blank	-	6	38	tvp- tvv			

- **Note**: Hsync period and Hsync width-active should be even number times of tCLK. If the value is odd number times of tCLK, display control signal can be asynchronous. In order to operate this LCM a Hsync, Vsyn, and DE(data enable) signals should be used.
 - 1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
 - 2. Vsync and Hsync should be keep the above specification.
 - 3. Hsync Period, Hsync Width, and Horizontal Back Porch should be any times of of character number(8).



3-4. Signal Timing Waveforms





3-5. Color Data Reference

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The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

													Inpu	ut Co	olor	Data	a									
	Color					RE	D							GRE	EEN							BL	UE			
			MS								MS								MS							SB
	I								R1																	_
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN							-																			
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note : Users should be input true 8 Bit data streams via LVDS transmitter.



3-6. Power Sequence

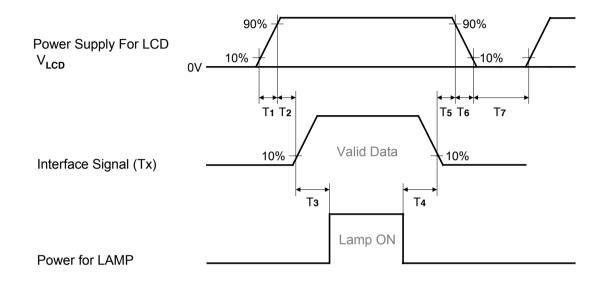
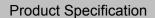


Table 9. POWER SEQUENCE

Deremeter		Values								
Parameter	Min	Тур	Max	Units						
T1	0.5	-	10	ms						
T2	5	-	50	ms						
Т3	200	-	-	ms						
T4	200	-	-	ms						
Т5	0.5	-	50	ms						
T6	-	-	100	ms						
Т7	1	-	-	s						

- Notes : 1. Please avoid floating state of interface signal at invalid period.
 - 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
 - 3. Lamp power must be turn on after power supply for LCD an interface signal are valid.



4. Optical Specification

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Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0[°].

FIG. 1 presents additional information concerning the measurement equipment and method.

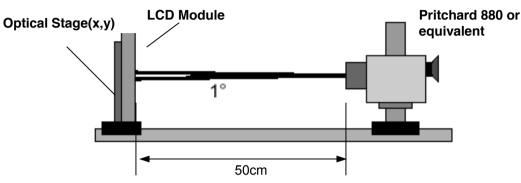


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10.	OPTICAL	CHARACTERISTICS
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Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=65MHz, I_{Lamp}=7mA

Param	ator	Cumb	al		Values		Units	Notes
Falalli	eter	Symb	0I	Min	Тур	Max	Units	Notes
Contrast Ratio		CR		360	450			1
Surface Luminanc	e, white	L _{WF}	1	340	410		cd/m ²	2
Luminance Variation	n	δ_{WHITE}	5P			1.3		3
Deenenee Time	Rise Time	Tr _R		-	12	20	ms	4
Response Time	Decay Time	Tr _D		-	13	20	ms	4
	RED	Rx			0.636			
		Ry			0.344			
	GREEN	Gx			0.285			
Color Coordinates		Gy		Тур	0.609	Тур		
[CIE1931]	BLUE	Bx		-0.03	0.146	+0.03		
		By			0.066			
	WHITE	Wx	[0.289	1		
		Wy	,		0.304	1		
Viewing Angle (CR	>10)							
x axis	s, right(φ=0°)	θr		85	89		degree	5
x axis	s, left (ϕ =180°)	θI		85	89			
y axis	s, up (φ=90°)	θu		85	89	1		
y axis	s, down (φ=270°)	θd		85	89			
Gray Scale					-			6

Notes 1. Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels

Contrast Ratio = -

= Surface Luminance with all black pixels

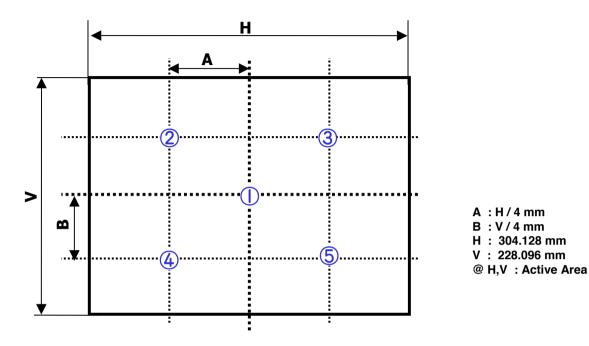
It is measured at center point(1)

- 2. Surface luminance is luminance value at center point (1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3},, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3},, L_{on5}) Where L_{on1} to L_{on5} or L_{on13} are the luminance with all pixels displaying white at 5,13 locations . For more information see FIG 2.
- 4. Response time is the time required for the display to transition from black to white(Rise Time, Tr_R) and from white to black(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification Gamma Value is approximately 2.2. For more information see Table 11.

Gray Level	Luminance [%] (Typ)
LO	0.20
L15	0.40
L31	1.20
L47	2.35
L63	4.02
L79	7.18
L95	11.3
L111	16.1
L127	21.3
L143	27.9
L159	35.9
L175	44.9
L191	54.5
L207	65.2
L223	76.9
L239	88.6
. L255	100

Table 11. Gray Scale Specification





Measuring point for surface luminance & measuring point for luminance variation

FIG. 2 Measure Point for Luminance

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

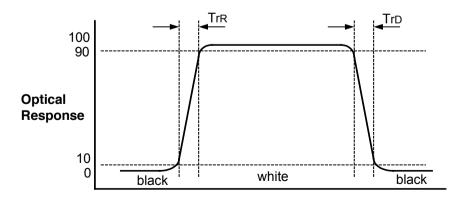


FIG. 3 Response Time



Dimension of viewing angle range

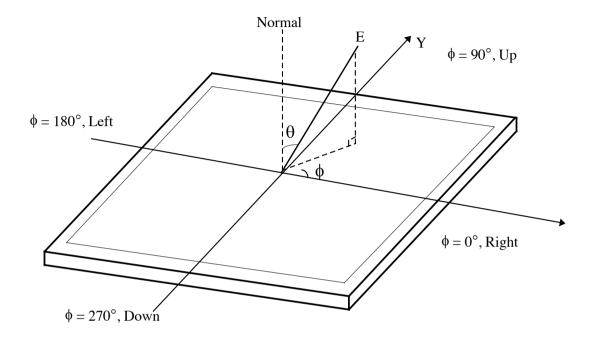


FIG. 4 Viewing angle



5. Mechanical Characteristics

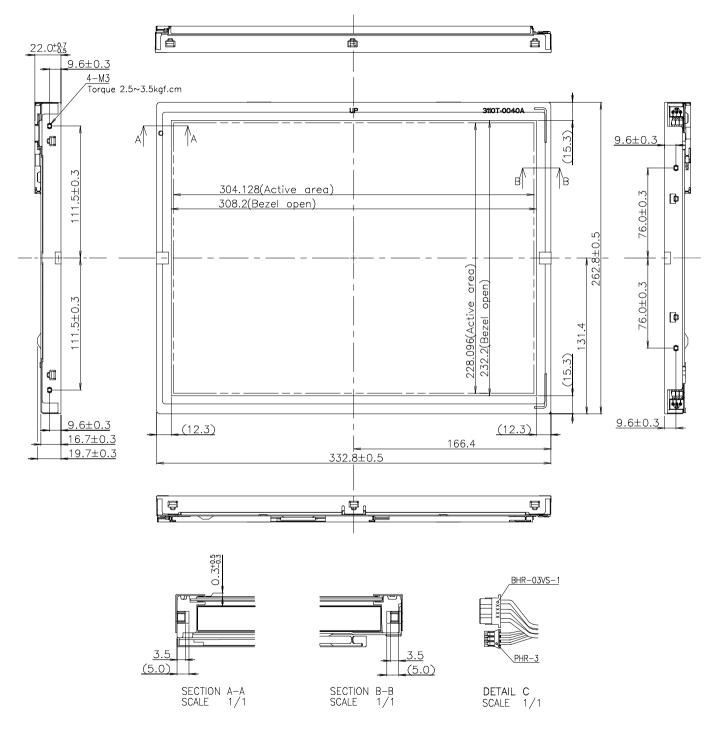
The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	332.8mm				
Outline Dimension	Vertical	262.8mm				
	Depth	22.0 mm				
Bezel Area	Horizontal	308.2 mm				
Dezel Alea	Vertical	232.2 mm				
Active Display Area	Horizontal	304.128mm				
Active Display Area	Vertical	228.096mm				
Weight	2,100g (Typ.), 2,250g (Max.)					
Surface Treatment	Hard coating(3H) Anti-glare(13%) treatment of the front polarizer					

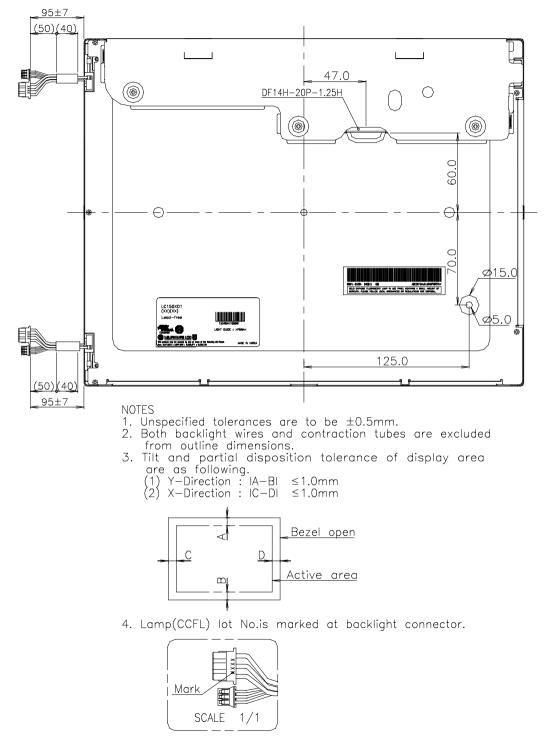
Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.



<FRONT VIEW>



<REAR VIEW>



5. Do not wrap conductive tapes around the backlight wires.



6. Reliability

Environment test condition

LG.PHILIPS LCD 🥰

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration : X,Y,Z, 20 min One time each direction
6	Shock test (non-operating)	Shock level : 120G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Altitude operating storage / shipment	0 - 10,000 feet(3048m) 0 - 40,000 feet(12,192m)



7. International Standards

7-1. Safety

- a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000. Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000. Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- c) EN 60950 : 2000, Third Edition IEC 60950 : 1999, Third Edition European Committee for Electrotechnical Standardization(CENELEC) EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

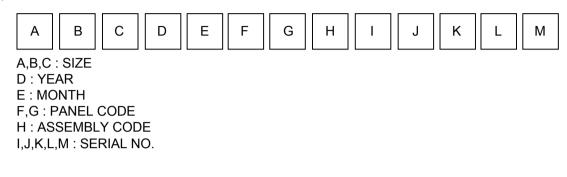
- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

3. Serial No.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 7 pcs
- b) Box Size : 443mm imes 375mm imes 350mm

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 - V= ± 200 mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- The protection film is attached to the bezel with a small masking tape.
 When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



Warranty for Non-Inclusion of Hazardous Substances in Products (ver.4.1) Our company hereby warrants and guarantees that all of or part of products, including, but not limited to, the peripherals, accessories or (including your subsidiaries and affiliated company) directly or packages, manufactured and/ or delivered to your company indirectly by our company (including our subsidiaries or affiliated companies) are free from any of the substances listed in your company's Technology Standard or its subsequent revision, including the following articles. 1. Our company actually cooperates with environment-friendly policy pushed by your company and follows the total abolition schedule of Pb, Cd, Cr+6, Hg, PBB and PBDEs proposed by Hazardous substances management standard in your company 2. Our company don't excessively enclose to the noxious material including Cd, Pb in products and parts supporting to the department of DID in LG. electronics. If the environmental accident is occurred by our company's mistake and insufficient improvement, acts on our company's responsibility 3. Dur company ensure that all of information submitted to your company is not arbitrary estimation and is to the exact document based on reliable. data. By attached Data. Our company warrants this products comfirm to following requirements. (Check each applicable item) Pb Free RoHS Free I TCO '03 Pb Free Soldering Definition Pb Free Soldering : Parts must meet DID's Heat resistance condition. (Reflow Type: 250°C, tDsec, Flow Type / 260°C, 10sec) Po Free : Lead wire plating is Pb Free(Pb content < 1,000ppm) and</p> Inner contact point and Body itself don't contain Pb(Pb contant < 1,000ppm) RoHS Free : Part does not contain Pb,Cd,C+6,Hg,PBBe and PBDEs(Cd<100ppm,Others<1,000ppm)</p> and supplier must submit evidences(ICP,AAS,UV-Vis,GC) certificated by authorised agency TOD '03 - Part meet TCO'03 and LG electronics requirement (Cd<Sppm, Pb<50ppm, Hg<2ppm, Cr+6, PBBs, PBDEs<100) Date : Jan 11. 2007 Mun- Cheel Representative : Signature