

5. Input Terminals

5-1) TFT-LCD Panel Driving

Connector type: Molex 55177-1491

Pin No.	Symbol	Function	Remark
1	VDD	Power supply : +3.3V	
2	VDD	Power supply : +3.3V	
3	GND		
4	GND		
5	IN0-	Pixel data Transmission pair 0 (negative -)	
6	IN0+	Pixel data Transmission pair 0 (positive +)	
7	IN1-	Pixel data Transmission pair 1 (negative -)	
8	IN1+	Pixel data Transmission pair 1 (positive +)	
9	IN2-	Pixel data Transmission pair 2 (negative -)	
10	IN2+	Pixel data Transmission pair 2 (positive +)	
11	CLK-	Sampling Clock (negative -)	
12	CLK+	Sampling Clock (positive +)	
13	GND		
14	GND		

Recommended Transmitter (DS90C\*363 of National Semiconductor) to V26L8060BA interface Assignment:

Input terminal of DS 90C*363		Graphic controller output signal		Output signal symbol	To V26L8060AA interface terminal(Symbol)
Symbol	No.	Symbol	Function		
TIN0	44	R0	Red pixel data (LSB)	Tout0- Tout0+	No.5 : IN0- No.6 : IN0+
TIN1	45	R1	Red pixel data		
TIN2	47	R2	Red pixel data		
TIN3	48	R3	Red pixel data		
TIN4	1	R4	Red pixel data		
TIN5	3	R5	Red pixel data(MSB)	Tout1- Tout1+	No.7 : IN1- No.8 : IN1+
TIN6	4	G0	Green pixel data (LSB)		
TIN7	6	G1	Green pixel data		
TIN8	7	G2	Green pixel data		
TIN9	9	G3	Green pixel data		
TIN10	10	G4	Green pixel data	Tout2- Tout2+	No.9 : IN2- No.10 : IN2+
TIN11	12	G5	Green pixel data(MSB)		
TIN12	13	B0	Blue pixel data(LSB)		
TIN13	15	B1	Blue pixel data		
TIN14	16	B2	Blue pixel data		
TIN15	18	B3	Blue pixel data	TCLK out- TCLK out+	No.11 : CLK IN- No.12 : CLK IN+
TIN16	19	B4	Blue pixel data		
TIN17	20	B5	Blue pixel data(MSB)		
TIN18	22	NC	No connection		
TIN19	23	NC	No connection		
TIN20	25	ENAB	Compound Synchronization signal		
CLK in	26	NCLK	Data sampling clock		

Data stream of IN0-/+, IN1-/+, and IN2-/+, for V26L8060BA