

Sharp LJ64ZU49/50/51/52 640 X 480 EL 16 Gray Level

Signal	Pin No.	PC530 Signal	J8	J9	Description
N/C	A1				
N/C	B1				
D11	A2	PNL5	11	11	Data
D10	B2	PNL4	9	9	Data
D13	A3	PNL7	15	15	Data
D12	B3	PNL6	13	13	Data
D01	A4	PNL1	3	3	Data
D00	B4	PNL0	1	1	Data
D03	A5	PNL3	7	7	Data
D02	B5	PNL2	5	5	Data
N/C	A6				
N/C	B6				
CKD	A7	XCLK	21	41	Data Shift Clock
GND	B7	GND	2	4	Ground
HD	A8	VEE/DE,DE	25	40	Data Latch (Hsync)
GND	B8	GND	4	6	Ground
VD	A9	OFLM	17	38	Scan Start Up (Vsync)
GND	B9	GND	6	10	Ground
GND	A10	GND	8	16	Ground
GND	B10	GND	10	20	Ground
VL#	A11	+12FUSED	24	12	External +24VDC
VL#	B11	+12FUSED	26	14	External +24VDC
VL	A12	+5FUSED	18	26	+5V Logic Power
VL	B12	+5FUSED	18	26	+5V Logic Power
VD*	A13	+12FUSED	24	12	+12V Power
VD*	B13	+12FUSED	26	14	+12V Power

Jumper Settings: J5 2-3, J12 2-3, J16 2-3, J6 1-2, J7 1-2, J11 2-3

Note: J5 and J12 are not installed on the PC525.

For LJ64ZU49 which requires external +24VDC. These pins are not connected on +12VDC versions. (See [J10 Power Connector](#) description.)

* These pins are not connected on +24 VDC versions.